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## PARTHA GARAI

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Working as an Assistant Professor at **Kalyani Government Engineering College**, West Bengal. Worked as an Assistant Professor at **National Institute of Science & Technology**, Odisha. Also worked in Machine Intelligence Unit, **Indian Statistical Institute**, Kolkata, as a **Junior Research Fellow** (Institute Fellowship and INSPIRE Fellowship), doing the research work on Pattern Recognition. Worked as an **Asst. System Engineer** (Application Programmer) in **IBM India Pvt. Ltd.** Also was in **Techno India College of Technology**, Rajarhat, as a **Lecturer** (M.C.A. Dept.) – **Research Associate** working in Wi-Fi project, under the guidance of **Prof. Pritimoy Bhattacharya**, **IIT Kharagpur**.

### INTERNATIONAL JOURNALS

1. Pradipta Maji and **Partha Garai**, "**Rough Hypercuboid Based Generalized and Robust IT2 Fuzzy C-Means Algorithm**", IEEE Transactions on Cybernetics, 51(7):3641-3652, 2021.
2. Pradipta Maji and **Partha Garai**, "**IT2 Fuzzy-Rough Sets and Max Relevance-Max Significance Criterion for Attribute Selection**", IEEE Transactions on Cybernetics, 45(8):1657-1668, 2015.
3. Pradipta Maji and **Partha Garai**, "**Simultaneous Feature Selection and Extraction Using Feature Significance**", Fundamenta Informaticae, 136:405-431, 2015.
4. Pradipta Maji and **Partha Garai**, "**Fuzzy-Rough Simultaneous Attribute Selection and Feature Extraction Algorithm**", IEEE Transactions on Cybernetics, 43(4):1166-1177, 2013.
5. Pradipta Maji and **Partha Garai**, "**On Fuzzy-Rough Attribute Selection: Criteria of Max-Dependency, Max-Relevance, Min-Redundancy, and Max-Significance**", Applied Soft Computing, 13(9):3968-3980, 2013.
6. Chaitali Biswas Dutta, **Partha Garai**, and Amitabha Sinha, "**Design of a Reconfigurable DSP Processor with Bit Efficient Residue Number System**", International Journal of VLSI Design & Communication Systems, 3(5):175-189, 2012.
7. **Partha Garai**, and Chaitali Biswas Dutta, "**Digital and Mixed Signal Testing Technology, The Standards IEEE 1149.1 and IEEE 1149.4**", International Journal of Computer & organization Trends, 1(3):38-42, 2011.
8. Chaitali Biswas Dutta, **Partha Garai**, and Amitabha Sinha, "**Estimation of Hardware Complexity of Residue Number System for Signal Processing Application**", International Journal of Computer Technology and Applications, 2(5):1540- 1547, 2011.

## INTERNATIONAL CONFERENCES/WORKSHOPS

1. **Partha Garai** and Pradipta Maji, "**Identification of Co-Expressed microRNAs Using Rough Hypercuboid Based Interval Type-2 Fuzzy C-Means Algorithm**", In Proceedings of the International Conference on Advanced Computing and Intelligent Engineering (ICACIE '16), pages 1-10, Odisha, India, Dec-2016.
2. **Partha Garai** and Pradipta Maji, "**Clustering of microRNAs Using Rough Hypercuboid Based Fuzzy C-Means**", In Proceedings of the 15th International Conference on Information Technology (ICIT '16), pages 304-308, Odisha, India, Dec-2016.
3. **Partha Garai** and Chaitali Biswas Dutta, "**RNS Based Reconfigurable Processor for High Speed Signal Processing**", 35<sup>th</sup> International IEEE Region 10 Conference (TENCON 2014), Bangkok, Thailand, pages 1-6, Oct-2014
4. Pradipta Maji and **Partha Garai**, "**Simultaneous Feature Selection and Extraction Using Fuzzy-Rough Sets**", 2<sup>nd</sup> International Conference on Soft Computing for Problem Solving (SocProS 2012), India, pages 115-123, Dec-2012.
5. Pradipta Maji and **Partha Garai**, "**Fuzzy-Rough MRMS Method for Relevant and Significant Attribute Selection**", 14<sup>th</sup> International Conference on Information Processing and Management of Uncertainty in Knowledge-Based Systems, Catania, Italy, pages 310-320, July-2012
6. Chaitali Biswas Dutta, **Partha Garai**, and Amitabha Sinha, "**A Scheme For Improving Bit Efficiency For Residue Number System**", VLSI- 2012, Chennai, India, July-2012
7. **Partha Garai**, Chaitali Biswas Dutta, and Amitabha Sinha, "**A Novel Architecture of A Reconfigurable DSP Processor Using RNS**", International Conference on Communications, Computers and Devices, Dec-2010
8. Partha Garai, Latin American eScience Workshop 2013, Sao Paulo, Brazil, May-2013

## EXPERIENCES

1. **Assistant Professor** at **Kalyani Government Engineering College**, West Bengal (July 2015 onwards)
2. **Assistant Professor** at **National Institute of Science & Technology**, Odisha (July 2013 – July 2015)
3. **Junior Research Fellow** (INSPIRE Fellowship) in Machine Intelligence Unit, **Indian Statistical Institute**, Kolkata for Two years and Nine months (Jan 2010 – July 2013).
4. **Associate System Engineer** (Application Programmer) in **IBM India Pvt. Ltd** for Two years and Nine months (Apr 2007 – Jan 2010).
5. **Lecturer (M.C.A. Dept.) – Research Associate** at **Techno India College of Technology**, Rajarhat for Nine months (Aug 2006 – Apr 2007).

## MAJOR ACHIEVEMENTS

1. Received **INSPIRE** Fellowship from **Department of Science & Technology, Government of India** given for doctoral studies to the first Rank Holders of Post Graduate programs in any Science stream from any recognized university.
2. Third position in All India basis examination of **Junior Research Fellowship, 2009** conducted by **Indian Statistical Institute**.
3. Qualified **UGC NET Examination in Computer Science and Applications**, June-2014
4. Scored **95.82 Percentile** in **GATE 2009** in **Computer Science & Engineering**.

## EDUCATIONAL QUALIFICATION

NAME OF THE EXAMINATION	BOARD / UNIVERSITY	YEAR OF PASSING
Ph.D in Computer Science	Indian Statistical Institute	2017
M.Tech. (IT)	West Bengal University of Technology	2009
M.C.A.	West Bengal University of Technology	2006
Graduation (Phys Hons)	Calcutta University	2002
Class XII (Science Stream)	West Bengal Council of Higher Secondary Education	1999
Class X	West Bengal Board of Secondary Education	1997

## PERSONAL DETAILS

Name : **Partha Garai**

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*The information given above is true and I'm solely responsible for its accuracy.*

Date : September 18, 2022

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(Partha Garai)